

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	("20050179404").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/10 11:42
L2	0	("1andtransducerandaudi\$5").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/10 11:43
L3	1	1 and transducer and audi\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/10 12:41
L4	1	1 and location and (duty duties)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/10 14:20
L5	2	("6771029").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/10 14:21
S1	40	((("5,925,990") or ("6,181,086") or ("6,388,396") or ("4,467,314") or ("5,352,957") or ("5,453,738") or ("5,544,037") or ("5,554,979") or ("5,565,855") or ("5,661,347") or ("5,675,221") or ("5,866,992") or ("6,094,016") or ("6,114,970") or ("6,118,231") or ("6,388,400") or ("6,388,404") or ("6,507,158") or ("6,583,573") or ("6,771,029"))).PN. or ((2001/0040805) or (2003/0001516) or (2003/0001522) or (2003/0107332) or (2002/0065583) or (2002/0154025) or (2003/0020595) or (2003/0036807) or (2003/0048626)).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/07 14:20
S2	104	(345/587).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 11:44
S3	2	("20050179404").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/07 12:24

S4	1	S3 and bidirection\$3 and terminal\$1 and two\$1state and sequence and (set near4 point) and port\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 12:44
S5	0	(340/38,40,41,42,71).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 14:25
S6	1879	(307/38,40,41,42,71).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 14:26
S7	51	S6 and bi\$1direction\$3 and (micro\$1controller controller processor micro\$1processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 15:50
S8	1676	(340/468,286.02,286.01,332).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 15:49
S9	30	S8 and bi\$1direction\$3 and (micro\$1controller controller processor micro\$1processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 15:54
S10	30	S9 not S7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 15:52
S11	425529	(interfac\$3 terminal\$1 bi\$1direction\$3) same (micro\$1controller controller processor micro\$1processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:09
S12	217393	S11 and (invert\$3 convert\$4 \$5bridge)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 15:55
S13	97142	S12 and (lamp\$1 tube\$1 light\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 18:53
S14	73603	S13 and ((transmit\$5 receiv\$5 transceiv\$5 communicat\$5) same signal\$3) and (detect\$5 monitor\$3 sens\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:59
S15	1871	(315/312,324,323,324,325,322,316,318).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 16:04

S16	304	S15 and ((plural\$5 several) near4 (ballast\$1 circuit\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:22
S17	36	S15 and (bi\$1direction\$3 same signal\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:15
S18	25	S17 not S16	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:16
S19	6994	S14 and (bi\$1direction\$3 same signal\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:15
S20	100	S19 and ("315"/\$7).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:15
S21	78	S20 not (21 19)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:24
S22	1687	S19 and ((plural\$5 several) near4 (ballast\$1 circuit\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:22
S23	791	S22 and (input\$1port\$3 output\$1port ((inp ut output) near3 port\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:23
S24	772	S23 not (24 21 19)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:59
S25	348	S15 and ((transmit\$5 receiv\$5 transceiv\$5 communicat\$5) same signal\$3) and (detect\$5 monitor\$3 sens\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:59
S26	208	S25 not (27 24 21 19)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 18:53
S28	4359	(315/291,307,dig.4).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 18:50

S29	1348	S28 and (port\$3 interface terminal\$1) and ((transmit\$5 receiv\$5 transceiv\$5 communicat\$5) same signal\$3) and (detect\$5 monitor\$3 sens\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 18:52
S30	832	S29 and (micro\$1controller controller processor micro\$1processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 18:52
S31	822	S30 and (lamp\$1 tube\$1 light\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/07 18:53
S32	699	S31 not (29 27 24 21 19)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/08 14:14
S33	2	("4977353").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/08 14:14
S34	0	S33 and (terminal\$1 port\$1 interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/08 14:23
S35	18	((Mohammed near3 Helal) (Larry near4 Puckeridge)).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/08 14:32
S36	2	("6498440").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/08 14:32
S37	0	S36 and (switch\$3 inverter)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/08 16:26
S38	2	("5154504").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/08 16:26
S39	0	("6and(memory storage)").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/08 16:26
S40	1	S38 and (memory storage)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 13:55

S41	3300	(set near4 data) same (memory storage) same (information command message) same (port interface terminal\$1) same (micro\$1computer micro\$1controller micro\$1processor processor controller computer)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 14:41
S42	528	S41 and (lamp tube bulb)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 14:47
S43	501	S42 and program\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 14:20
S44	51	S43 and ((set near4 data) same (part portion) same (chang\$4 adjust\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 14:43
S45	450	S43 not S44	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 14:19
S46	229	(315/316).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 15:29
S47	88	S46 and program\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 15:30
S48	3483918	(set near4 data) same (memory storage) same (information command message) same (micro\$1computer micro\$1controller micro\$1processor processor controller computer)(port interface terminal\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 14:42
S49	3481070	((set near4 data) same (part portion) same (chang\$4 adjust\$5)) same (memory storage) same (information message) same (micro\$1computer micro\$1controller micro\$1processor processor controller computer)(port interface terminal\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 14:44

S50	408	((part portion) near8 (set near4 data) near8 (memory storage)) same ((chang\$4 adjust\$5) same (information message) same (micro\$1computer micro\$1controller micro\$1processor processor controller computer)(port interface terminal\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 15:40
S51	55	S50 and (inverter \$5bridge)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 15:39
S52	4	S51 and (lamp tube bulb)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 14:47
S53	728	(315/dig.4).ccis.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 15:30
S54	92	S46 and program\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 15:30
S55	149	S53 and program\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 15:31
S56	1	S55 and DALI	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 15:32
S57	2	("6771029").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/09 16:58
S58	0	S57 and (set near3 data)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 16:03
S59	1	S57 and (data same (memory storag\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 16:56
S60	2	("5154504").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/09 17:45

S61	0	S60 and digital\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/09 17:45
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